

Application No. 10/734,300  
Amendment dated October 30, 2007  
Reply to Office Action of June 8, 2007

Docket No.: SON-1697/DIV  
(80001-2913)

### **AMENDMENTS TO THE ABSTRACT**

#### **Please Amend the Abstract as follows:**

This invention relates to a digital/analog converter circuit, a level shift circuit, a shift-register containing this level shift circuit, a sampling latch circuit and a latch circuit as well as a liquid crystal display (LCD) device ~~mounted with these respective circuits. The LCD device, wherein a~~ includes an integrated drive circuit integrated with the LCD device containing the ~~and a~~ digital/analog converter circuit. The digital/analog converter circuit has a matrix of polysilicon thin film transistors ~~arrayed in a matrix positioned~~ on the substrate, as switching devices for the pixels. ~~The shift register includes [[,]] a level shift circuit in the shift register has a basic structure of CMOS latch cells and is utilized in each level shift of the clock signal at each transfer stage, and a sampling latch circuit with a basic structure of CMOS latch cells has a level shift function, and these. These respective~~ circuits may be incorporated into a single scanning type structural circuit with the drive circuit-integrated liquid crystal display LCD device to provide an LCD panel with ~~a~~ an extremely narrow picture frame, stable level shift operation, stable sampling & latch operation in a circuit ~~structure~~ having an extremely small number of components, low power consumption and a small surface area.